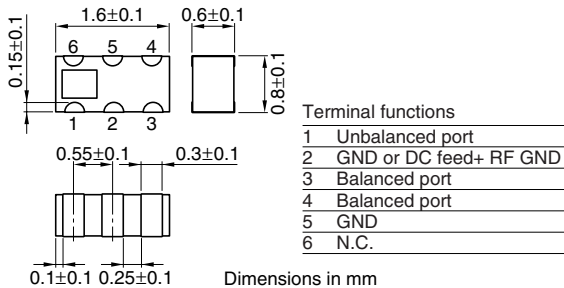


Multilayer Chip Baluns For UWB

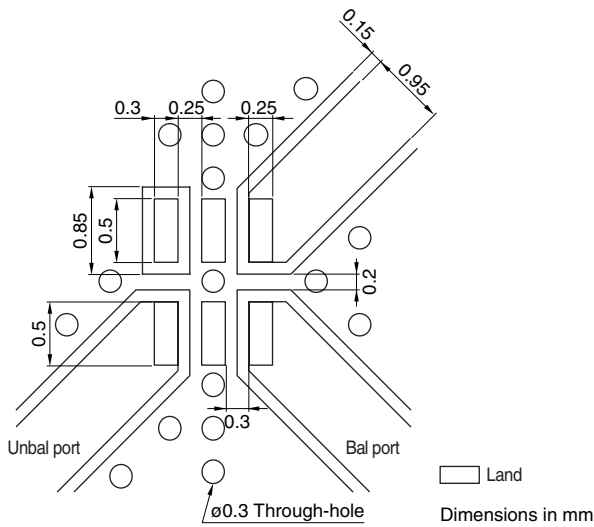
Conformity to RoHS Directive

HHM Series HHM1775B1

SHAPES AND DIMENSIONS



RECOMMENDED PC BOARD PATTERNS



Line width be designed to match 50 Ω characteristic impedance depending on PCB material and thickness.

ELECTRICAL CHARACTERISTICS

Unbalanced impedance	50 Ω	
Balanced impedance	100 Ω	
Frequency range	3100 to 4900MHz	
Unbalanced port return loss	10dB min.	
Phase imbalance at balanced port	180 \pm 15deg.	
Amplitude imbalance at balanced port	0 \pm 1.5dB	
Insertion loss	1.2dB max.	
Temperature range	Operating	-40 to +85 $^{\circ}$ C
	Storage	-40 to +85 $^{\circ}$ C
Packaging style and quantities	4000pieces/reel	

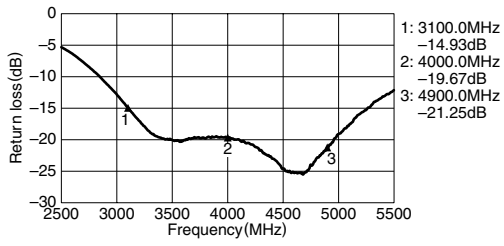
• Conformity to RoHS Directive: This means that, in conformity with EU Directive 2002/95/EC, lead, cadmium, mercury, hexavalent chromium, and specific bromine-based flame retardants, PBB and PBDE, have not been used, except for exempted applications.

• All specifications are subject to change without notice.

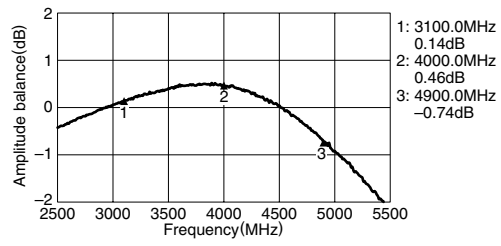
FREQUENCY CHARACTERISTICS

Unbalance 50Ω/Balance 100Ω

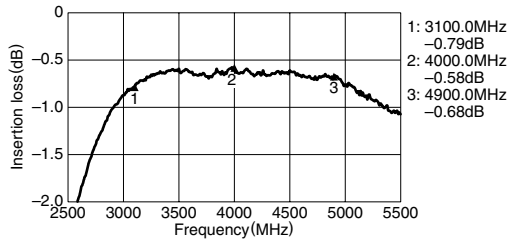
RETURN LOSS



AMPLITUDE BALANCE



INSERTION LOSS



PHASE BALANCE

